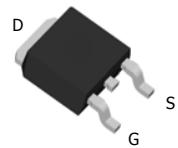


## N-Channel Enhancement Mode Power MOSFET

### Description

The K15N10G uses advanced trench technology to provide excellent  $R_{DS(ON)}$ , low gate charge and operation with gate voltages as low as 4.5V. This device is suitable for use as a Battery protection or in other switching application.

### TO-252



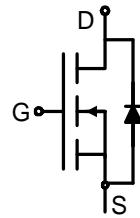
### General Features

- 

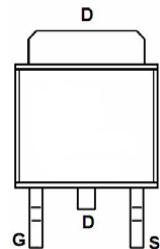
$V_{DSS}$	$R_{DS(ON)}$ @10V (typ)	$I_D$
100V	120mΩ	12A

- High power and current handing capability
- Lead free product is acquired
- Surface mount package

### Equivalent Circuit



### MARKING



### Application

- Battery switch
- DC/DC converter

### Absolute Maximum Ratings ( $T_A=25^\circ\text{C}$ unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	$V_{DS}$	100	V
Gate-Source Voltage	$V_{GS}$	$\pm 20$	V
Drain Current-Continuous $T_C=25^\circ\text{C}$	$I_D$	12	A
Drain Current-Continuous $T_C=100^\circ\text{C}$		8.4	A
Drain Current-Pulsed <sup>(Note 1)</sup>	$I_{DM}$	30	A
Maximum Power Dissipation $T_C=25^\circ\text{C}$	$P_D$	30	W
Maximum Power Dissipation $T_C=100^\circ\text{C}$		15	W
Operating Junction and Storage Temperature Range	$T_J, T_{STG}$	-55 To 175	°C

### Thermal Characteristic

Thermal Resistance, Junction-to-Case <sup>(Note 2)</sup>	$R_{\theta JC}$	5	°C/W
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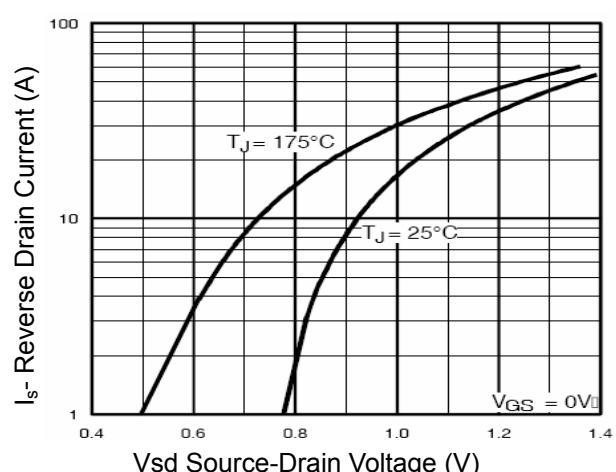
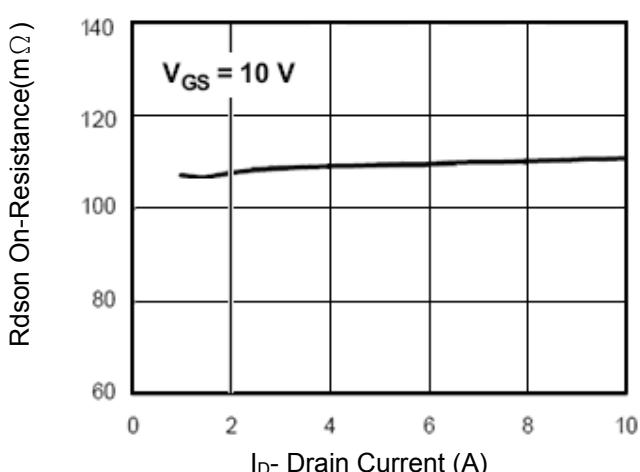
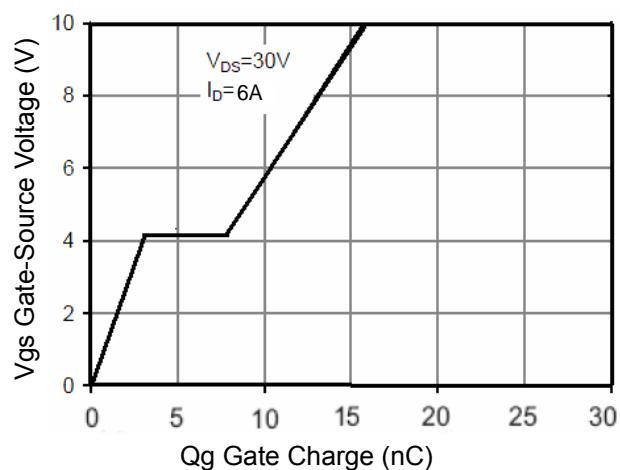
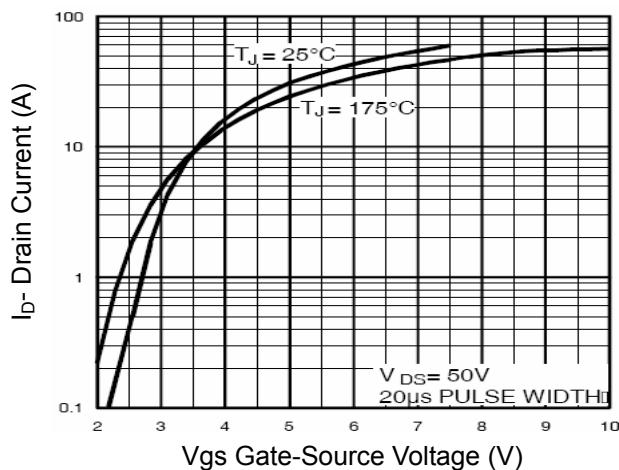
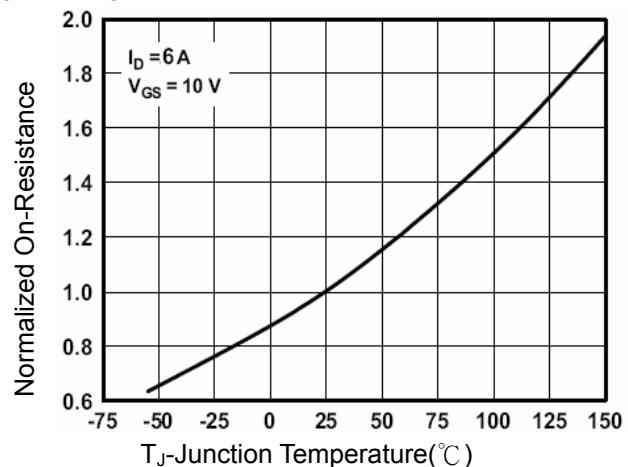
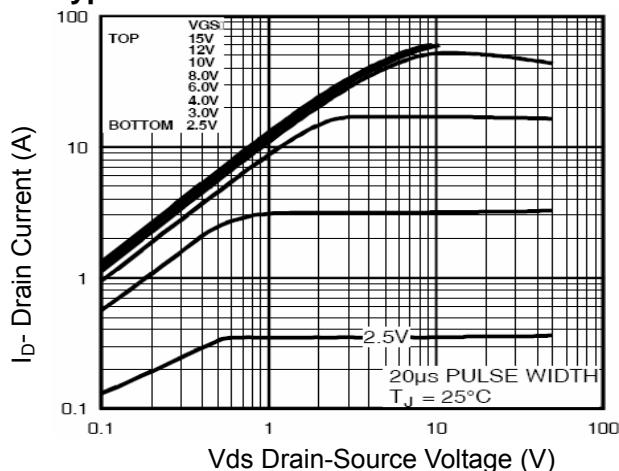
**Electrical Characteristics ( $T_c=25^\circ\text{C}$  unless otherwise noted)**

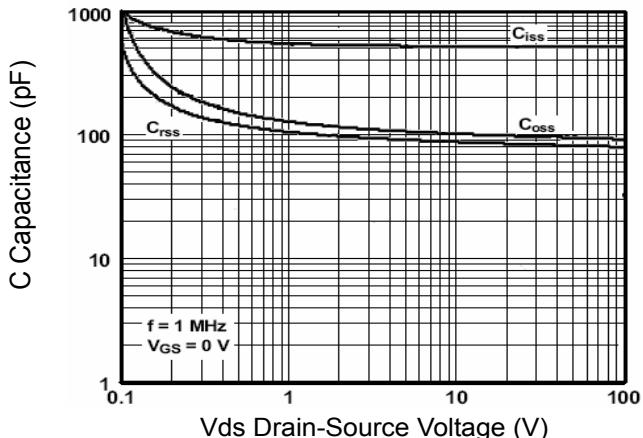
Parameter	Symbol	Condition	Min	Typ	Max	Unit
<b>Off Characteristics</b>						
Drain-Source Breakdown Voltage	$\text{BV}_{\text{DSS}}$	$V_{\text{GS}}=0\text{V}, I_{\text{D}}=250\mu\text{A}$	100	107	-	V
Zero Gate Voltage Drain Current	$I_{\text{DS}}^{\text{SS}}$	$V_{\text{DS}}=100\text{V}, V_{\text{GS}}=0\text{V}$	-	-	1	$\mu\text{A}$
Gate-Body Leakage Current	$I_{\text{GSS}}$	$V_{\text{GS}}=\pm 20\text{V}, V_{\text{DS}}=0\text{V}$	-	-	$\pm 100$	nA
<b>On Characteristics</b> <small>(Note 3)</small>						
Gate Threshold Voltage	$V_{\text{GS}(\text{th})}$	$V_{\text{DS}}=V_{\text{GS}}, I_{\text{D}}=250\mu\text{A}$	1.2	1.8	2.5	V
Drain-Source On-State Resistance	$R_{\text{DS}(\text{ON})}$	$V_{\text{GS}}=10\text{V}, I_{\text{D}}=6\text{A}$	-	0.120	0.136	$\Omega$
Drain-Source On-State Resistance	$R_{\text{DS}(\text{ON})}$	$V_{\text{GS}}=4.5\text{V}, I_{\text{D}}=6\text{A}$	-	0.130	0.146	$\Omega$
Forward Transconductance	$g_{\text{FS}}$	$V_{\text{DS}}=25\text{V}, I_{\text{D}}=6\text{A}$	3.5	-	-	S
<b>Dynamic Characteristics</b> <small>(Note 4)</small>						
Input Capacitance	$C_{\text{iss}}$	$V_{\text{DS}}=25\text{V}, V_{\text{GS}}=0\text{V}, F=1.0\text{MHz}$	-	206	-	PF
Output Capacitance	$C_{\text{oss}}$		-	28.6	-	PF
Reverse Transfer Capacitance	$C_{\text{rss}}$		-	1.6	-	PF
<b>Switching Characteristics</b> <small>(Note 4)</small>						
Turn-on Delay Time	$t_{\text{d}(\text{on})}$	$V_{\text{DD}}=50\text{V}, I_{\text{D}}=5\text{A}, V_{\text{GS}}=10\text{V}, R_{\text{G}}=2\Omega$	-	14.7	-	nS
Turn-on Rise Time	$t_r$		-	3.5	-	nS
Turn-Off Delay Time	$t_{\text{d}(\text{off})}$		-	20.9	-	nS
Turn-Off Fall Time	$t_f$		-	2.7	-	nS
Total Gate Charge	$Q_g$	$V_{\text{DS}}=50\text{V}, I_{\text{D}}=5\text{A}, V_{\text{GS}}=10\text{V}$	-	4.3	-	nC
Gate-Source Charge	$Q_{\text{gs}}$		-	1.5	-	nC
Gate-Drain Charge	$Q_{\text{gd}}$		-	1.1	-	nC
<b>Drain-Source Diode Characteristics</b>						
Diode Forward Voltage <small>(Note 3)</small>	$V_{\text{SD}}$	$V_{\text{GS}}=0\text{V}, I_{\text{S}}=9.6\text{A}$	-	-	1.2	V
Diode Forward Current <small>(Note 2)</small>	$I_{\text{S}}$		-	-	12	A
Reverse Recovery Time	$t_{\text{rr}}$	$T_J = 25^\circ\text{C}, IF = 5.6\text{A}$ $di/dt = 100\text{A}/\mu\text{s}$ <small>(Note 3)</small>	-	21		nS
Reverse Recovery Charge	$Q_{\text{rr}}$		-	97		nC
Forward Turn-On Time	$t_{\text{on}}$	Intrinsic turn-on time is negligible (turn-on is dominated by LS+LD)				

**Notes:**

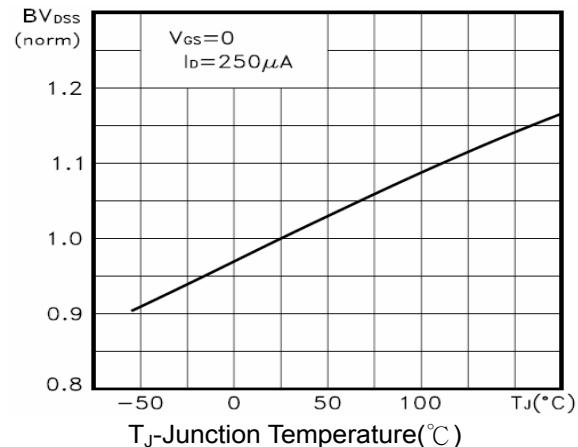
1. Repetitive Rating: Pulse width limited by maximum junction temperature.
2. Surface Mounted on FR4 Board,  $t \leq 10$  sec.
3. Pulse Test: Pulse Width  $\leq 300\mu\text{s}$ , Duty Cycle  $\leq 2\%$ .
4. Guaranteed by design, not subject to production

### Typical Electrical and Thermal Characteristics (Curves)

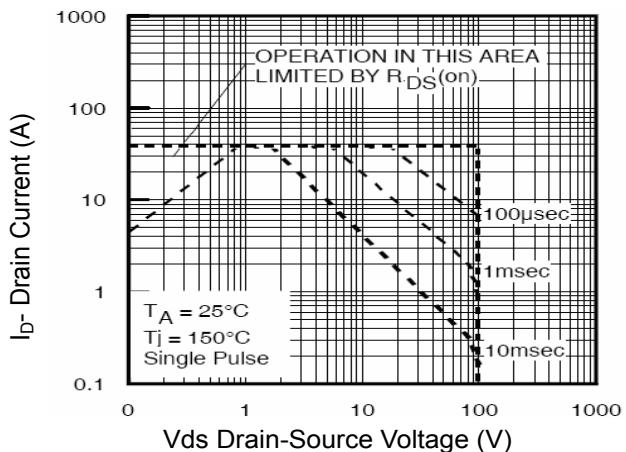




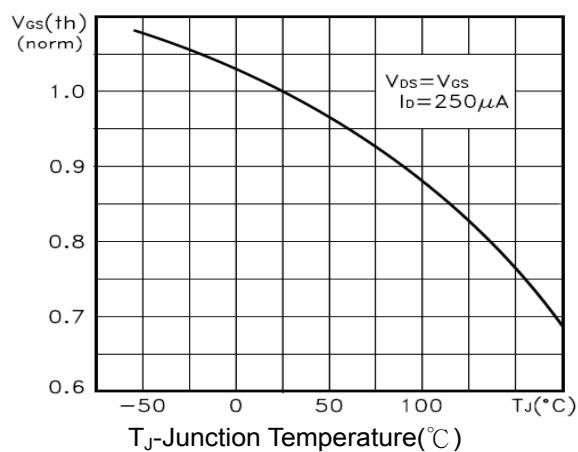
**Figure 7 Capacitance vs Vds**



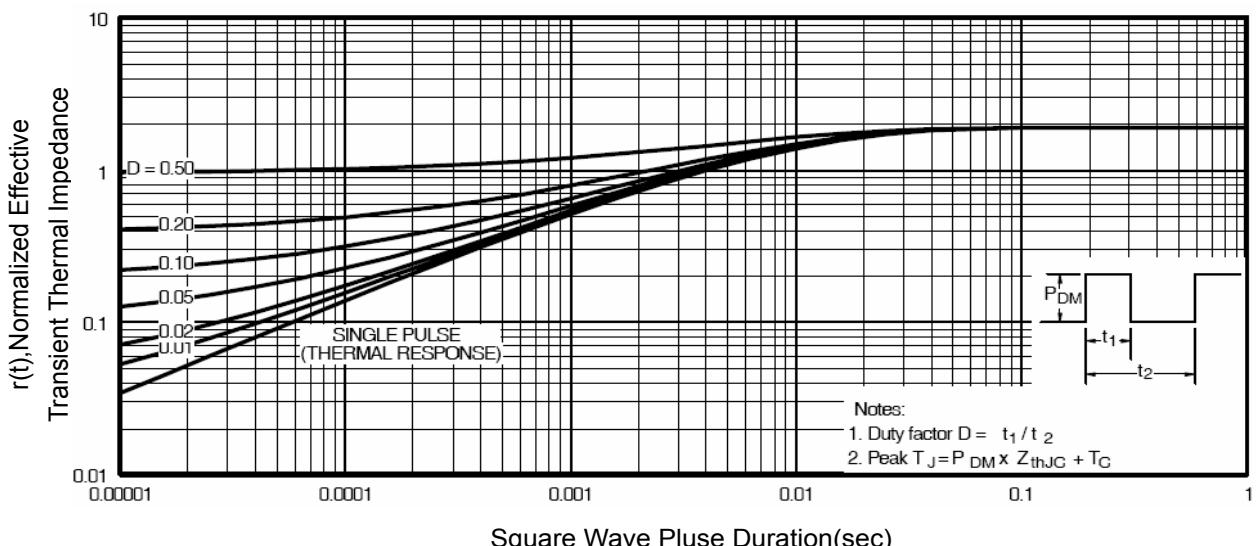
**Figure 9  $BV_{dss}$  vs Junction Temperature**



**Figure 8 Safe Operation Area**



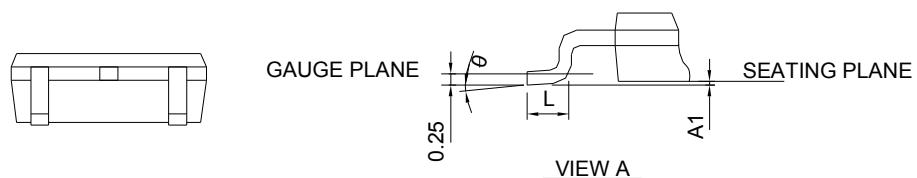
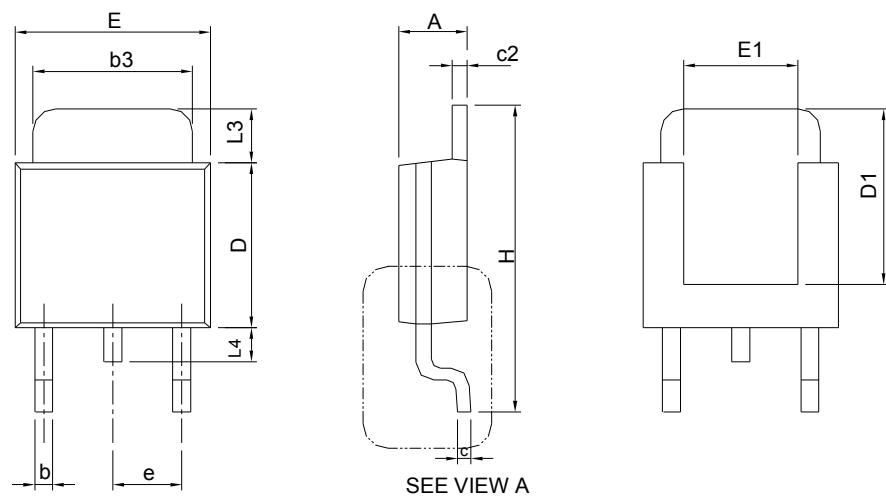
**Figure 10  $V_{gs(th)}$  vs Junction Temperature**



**Figure 11 Normalized Maximum Transient Thermal Impedance**

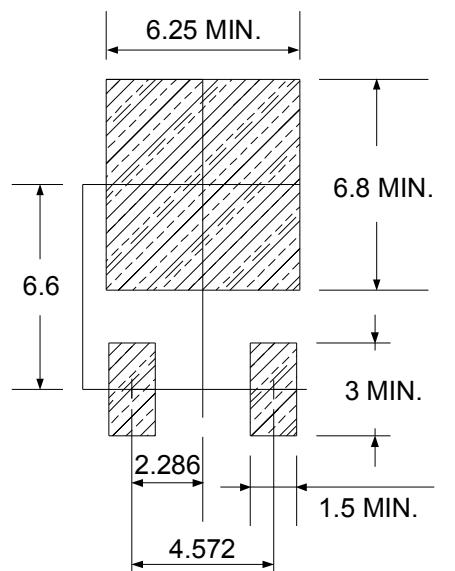
## Package Information

TO-252



SYMBOL	TO-252-3			
	MILLIMETERS		INCHES	
	MIN.	MAX.	MIN.	MAX.
A	2.18	2.39	0.086	0.094
A1		0.13		0.005
b	0.50	0.89	0.020	0.035
b3	4.95	5.46	0.195	0.215
c	0.46	0.61	0.018	0.024
c2	0.46	0.89	0.018	0.035
D	5.33	6.22	0.210	0.245
D1	4.57	6.00	0.180	0.236
E	6.35	6.73	0.250	0.265
E1	3.81	6.00	0.150	0.236
e	2.29 BSC		0.090 BSC	
H	9.40	10.41	0.370	0.410
L	0.90	1.78	0.035	0.070
L3	0.89	2.03	0.035	0.080
L4		1.02		0.040
θ	0°	8°	0°	8°

### RECOMMENDED LAND PATTERN



UNIT: mm